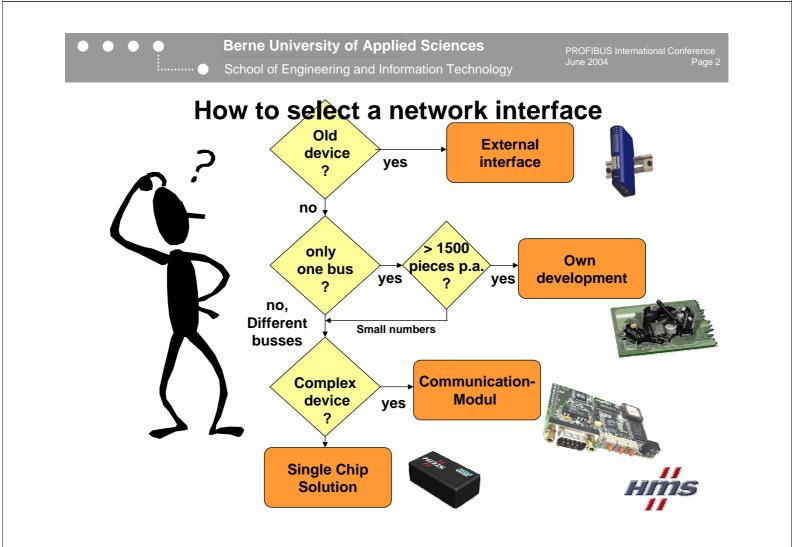
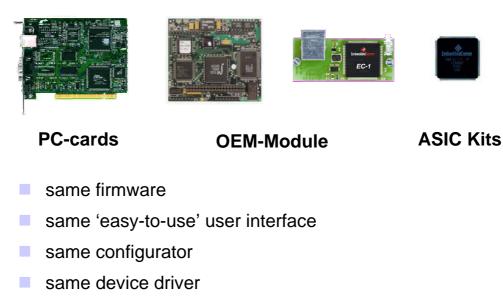
# **PROFIBUS** slave development

Prof. Max Felser Berne University of Applied Sciences School of Engineering and Information Technology Division of Electrical- and Communication Engineering Jlcoweg 1, CH-3400 Burgdorf, max.felser@bfh.ch



#### Set of Interfaces (Hilscher)



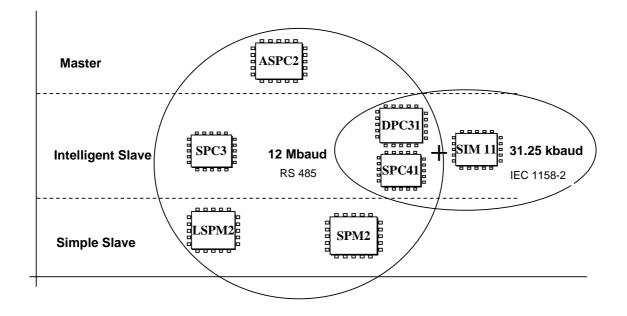
different formfactors



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# Set of ASICs (SIEMENS)



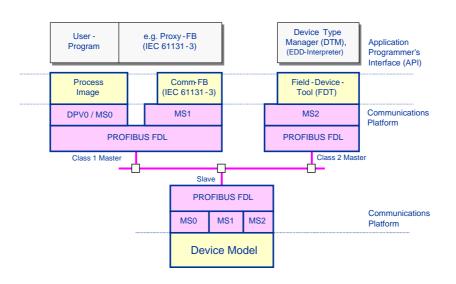
#### The application defines the requirements

Short Name	Application	Requirements	Bitrate / Speed
PA/GA	Processautomation Buildingautomation	Long distances Slow processes	93.75 KBit/s
MPI	Programming	Compatible with SIMATIC	187.5 KBit/s
FMS	Master-Master communication	Not for new systems	500 KBit/s
DP	Remote I/O	Efficient protocols	1.5 MBit/s
MC	Motion Control	Short and stable cycle times	12 Mbit/s

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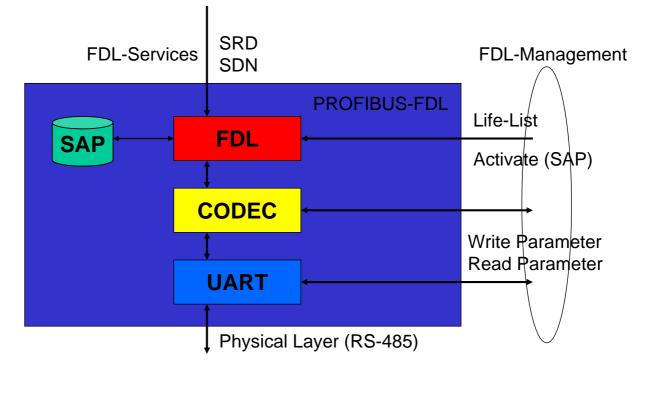
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#### **PROFIBUS DP System Structure**



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#### Possible internal structure of Fieldbus Data Link



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# Implementation Technologies

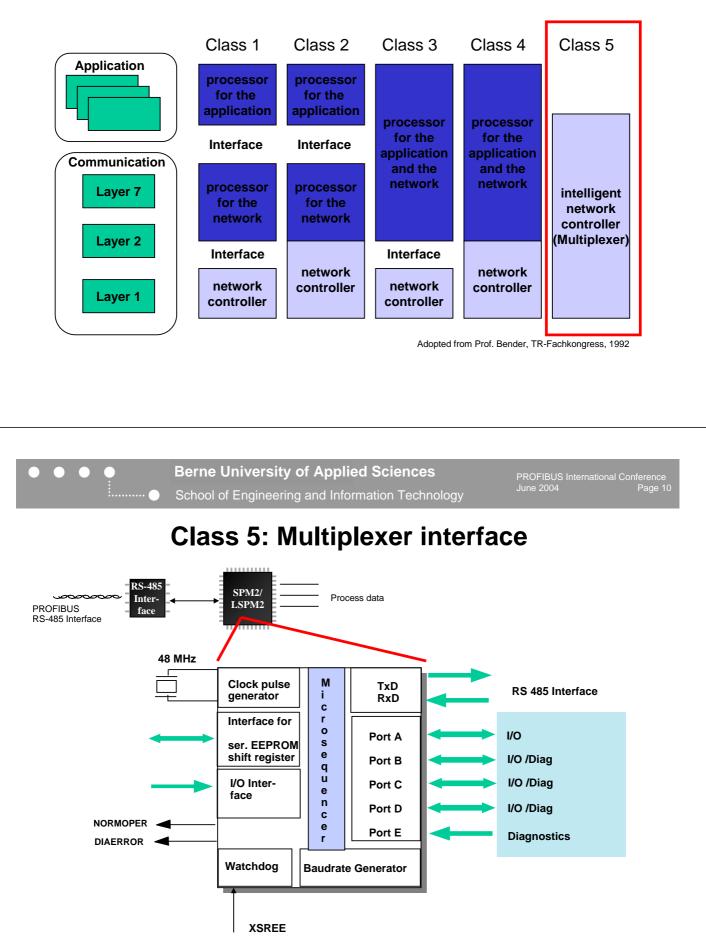
• Software (C)

- Hardware (ASICs / VHDL)
- Hybrid solutions

Bitrate	≤ 187.5 kBit/s	≤ 1.5 MBit/s	≤ 12 MBit/s
UART Block	HW	HW	HW
CODEC Block	SW	HW	HW
FDL Block	SW	SW	HW

For mass production and top speed special HW is mandatory

#### **Classification of network interfaces**



#### **Examples of Multiplexers (SIEMENS)**

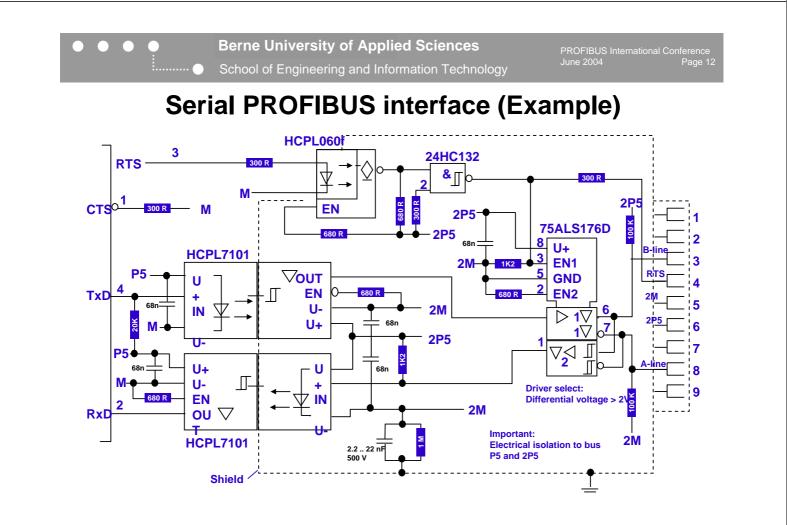


MQFB, 80 Pin, 2cm<sup>2</sup>

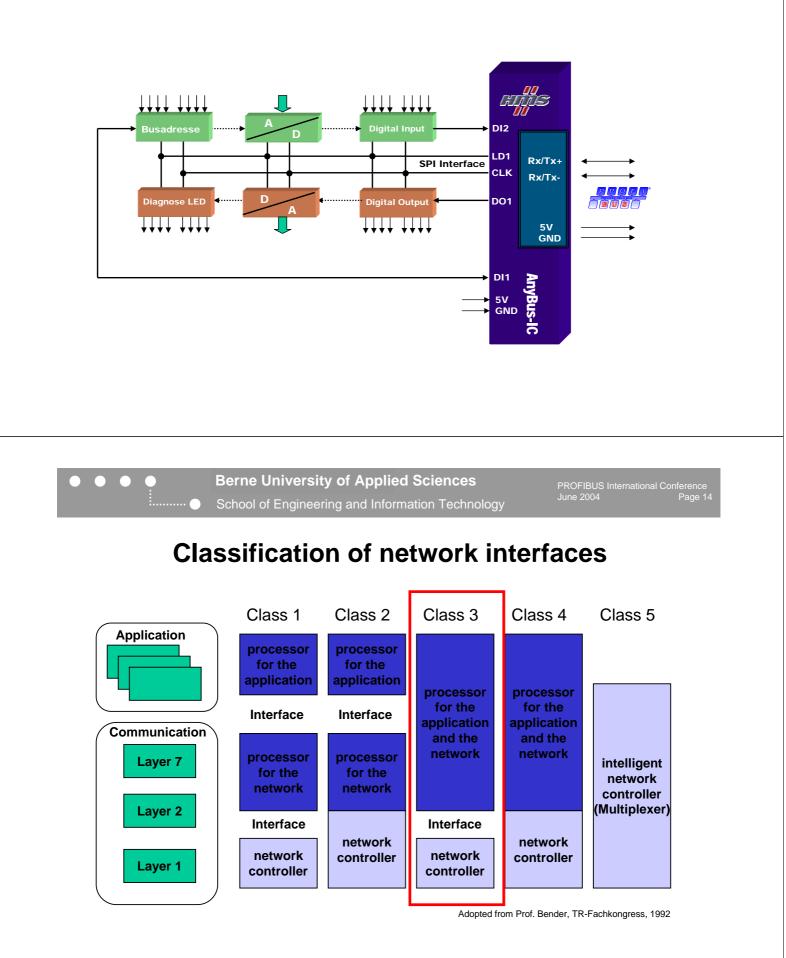
SPM2

PQFB, 120 Pin, 10cm<sup>2</sup>

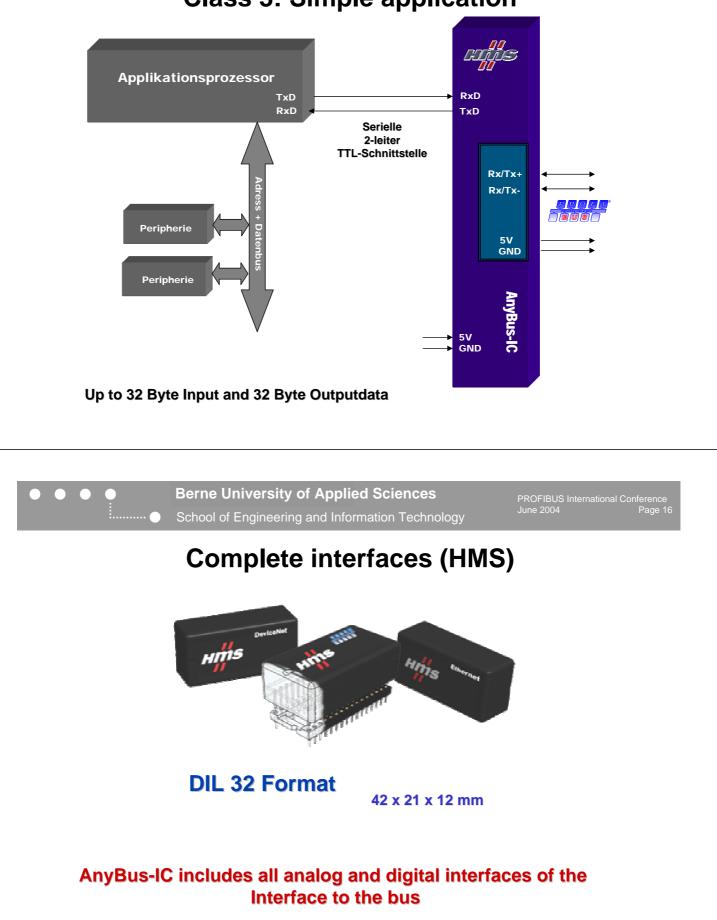
- Transmission rate up to 12Mbaud
- DP Protocol completely integrated
- No processor required
- Data Volume:
  - LSPM 2 32 bit I/O & 8 bit diagnostic SPM 2 - 64 bit I/O & 16 bit diagnostic



#### **Class 5: Multiplexer interface**



#### **Class 3: Simple application**



# Intelligent ASICs (SIEMENS)









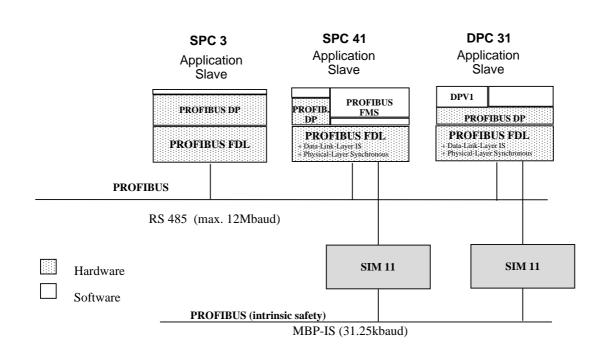
Max. Transmission Rate [Mbaud]	12	12	12	12
Transm. Medium	RS 485	RS485 / MBP	-IS (with SIM 11)	RS 485
Protocol	DP (DPV1)	DP/FMS/PA	DP/DPV1	DP/DPV1/FMS
Message Buffer	1.5	1.5	6	1,000
[kByte]				external
Housing	PQFP, 44 Pir	PQFP, 44 Pin	PQFP, 100 Pin	MQFP, 100 Pin



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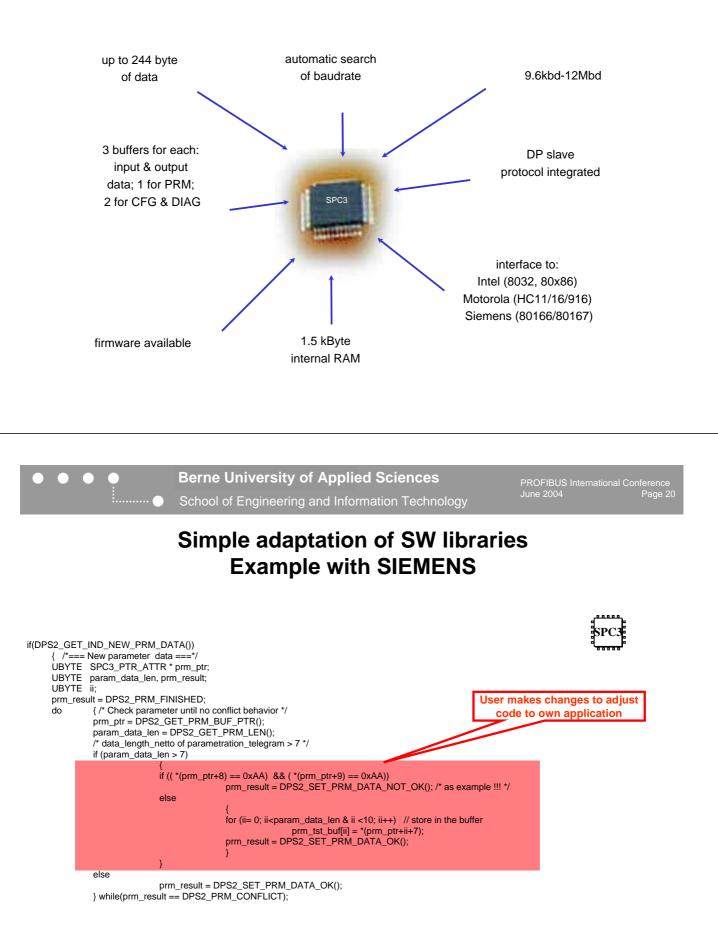
#### Available Firmware for the SIEMENS ASICs



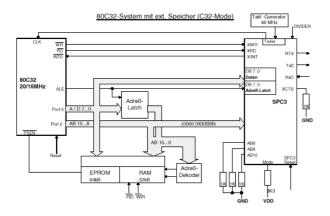
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#### One of the most used.... SPC3 (VPC3)



#### **PROFIBUS-DP – CAN Gateway for Motion-Controller**



The motors to turn the CDs are controlled over a PROFIBUS-DP – CAN gateway





Project with: Balzers Process Systems



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#### **PROFIBUS-DP** force interface in a connector



Singlechip 8051 controller with a SPC3

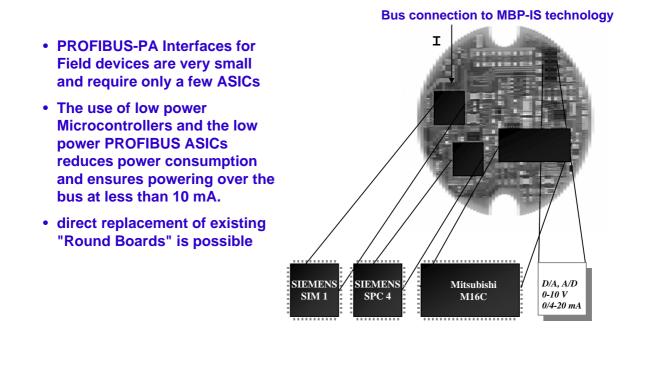




Project with: Force Measuring Systems AG



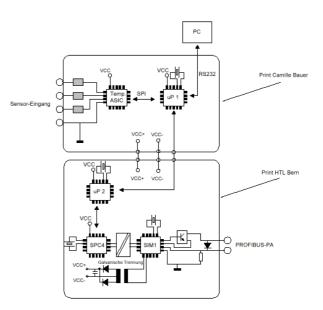
#### Class 3: Low power Interface (e.g. PROFIBUS-PA Interface for temperature transmitter)



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#### **PROFIBUS-PA** interface for temperatur transmitter

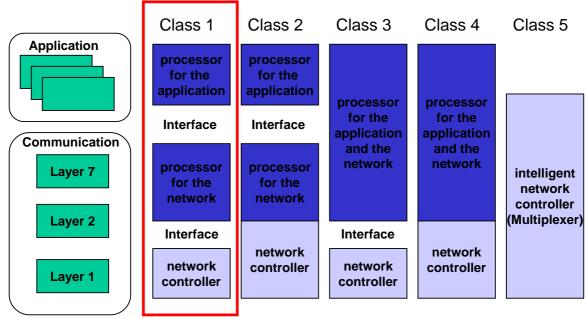






Project with: Camille Bauer AG, Wohlen

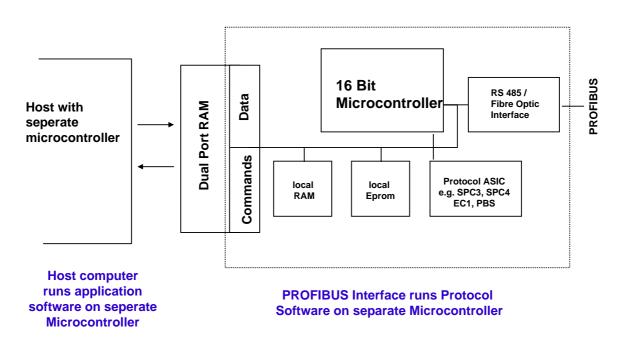
#### **Classification of network interfaces**



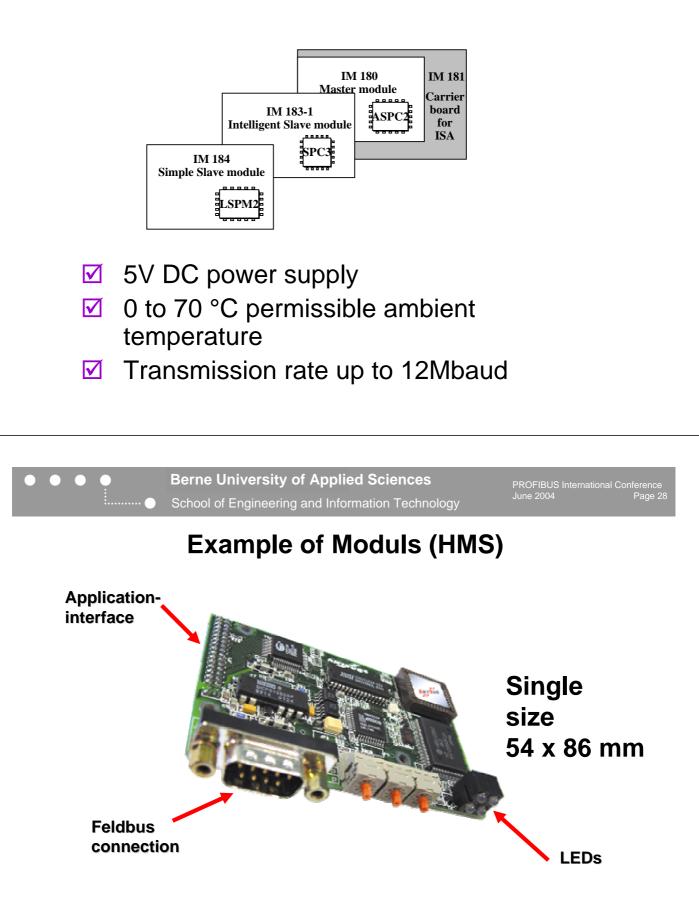
Adopted from Prof. Bender, TR-Fachkongress, 1992

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# Class 1: High performance interface (e.g. frequency converter with PROFIdrive profile)

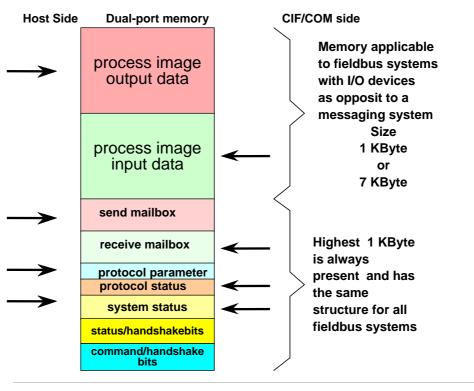


# **Available Moduls (SIEMENS)**



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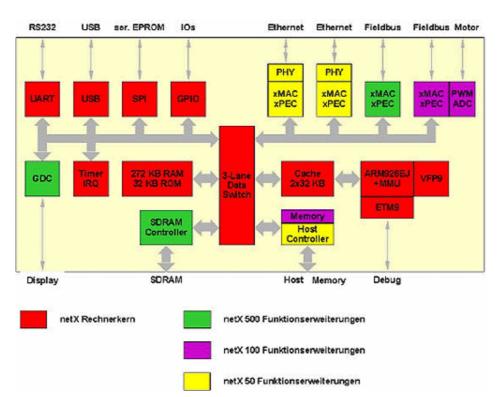
#### The Hilscher Dual-port Memory



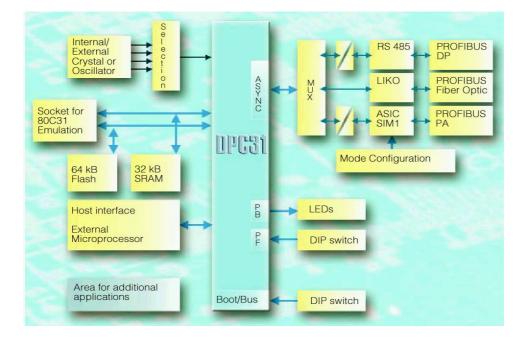
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# **NetX High performance ASIC**



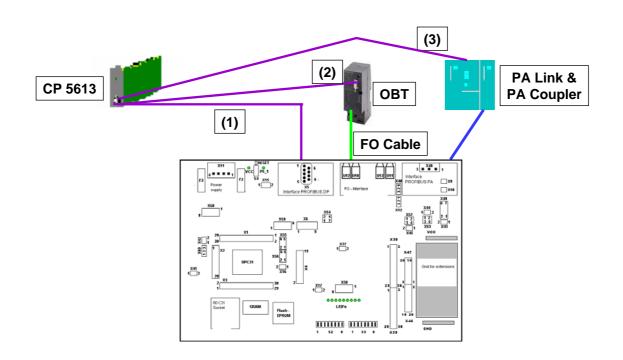
#### **Development Kit DP & PA - Test Board (SIEMENS)**



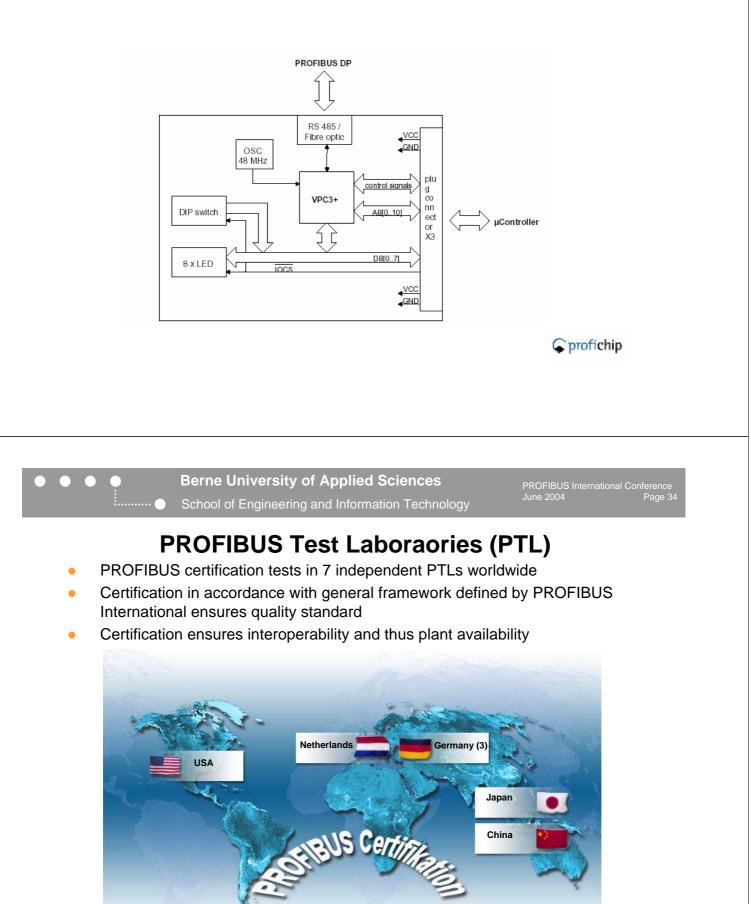
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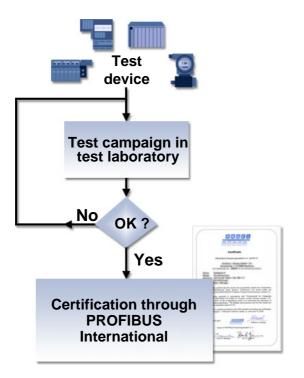
#### **Development Kit DP & PA - Test Board**



#### **Development Kit VPC3+ - Test Board (PROFIchip)**



#### **Certification Procedure**



#### Certification rules

- Uniform test measures and test process
- Comprehensible and documented results

More than 800 certificates awarded

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